

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	INTEGRATED CIRCUIT YIELD ENHANCEMENT USING VORONOI DIAGRAMS						
<p>Application Number :</p> <p>Confirmation Number:</p> <p>First Named Applicant: Robert Allen</p> <p>Attorney Docket Number: BUR920030135US1</p> <p>Art Unit:</p> <p>Examiner:</p> <p>Search string: (6178539 or 6247853 or 6317859).pn</p>							
US Patent Documents							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
NL	1	6178539	2001-01-23	Papadopoulou et al.			
NL	2	6247853	2001-06-19	Papadopoulou et al.			
NL	3	6317859	2001-11-13	Papadopoulou et al.			
Signature							
Examiner Name				Date			
Nelson L...				8/29/06			